

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : PHILLIPS ET AL. Confirmation No. : 6413  
Application No. : 09/927,426 Group Art Unit : 2816  
Filing Date : AUGUST 10, 2001 Examiner : WELLS, K. B.  
Docket No. : 95-L-024C3 (1678-041) Customer No. : 30431  
Title : DELAY CIRCUIT AND METHOD

SUPPLEMENTAL DECLARATION FOR REISSUE PATENT APPLICATION TO  
CORRECT "ERRORS" STATEMENT  
37 CFR §1.175

I/We hereby declare that:

Every error in the patent which was corrected in the present reissue application, and which is not covered by the prior oath(s) and/or declaration(s) submitted in this application, arose without any deceptive intention on the part of the applicant.

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under 18 USC §1001 and that such willful false statements may jeopardize the validity of the above identified application and/or any patent issued thereon.

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☐ Additional inventors are being named on the \_\_\_\_\_ supplemental sheets attached hereto.

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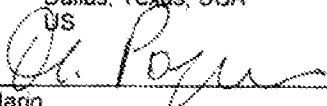
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